

TITLE: Apparatus and Methods for Fault-Tolerant Computing Using a Switching Fabric
APPLICANT: Long
ATTY. DOCKET NO.: SRT-022
EXPRESS MAIL NO.: EL692238020US
(Sheet 1 of 8)

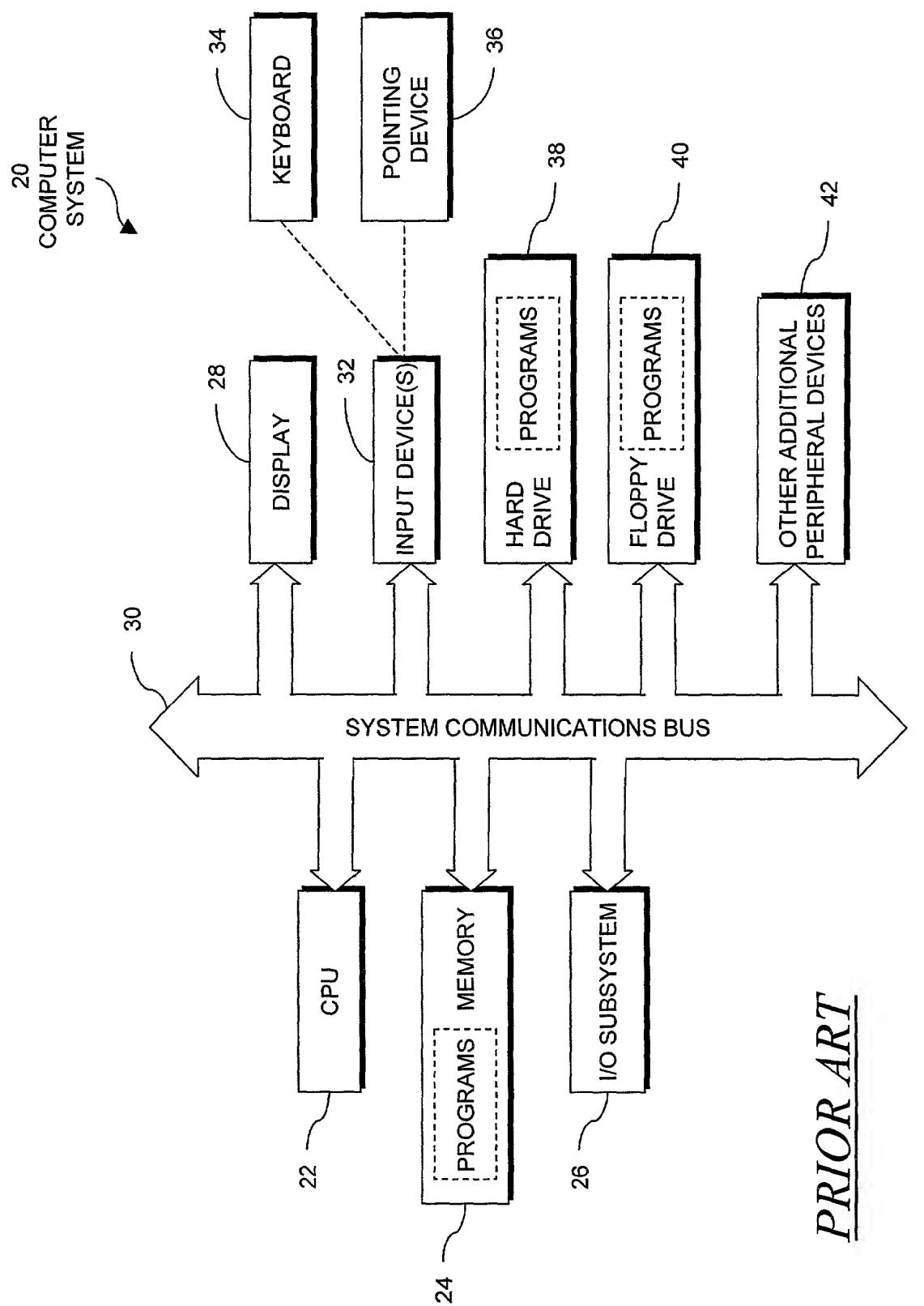


FIG. 1

PRIOR ART

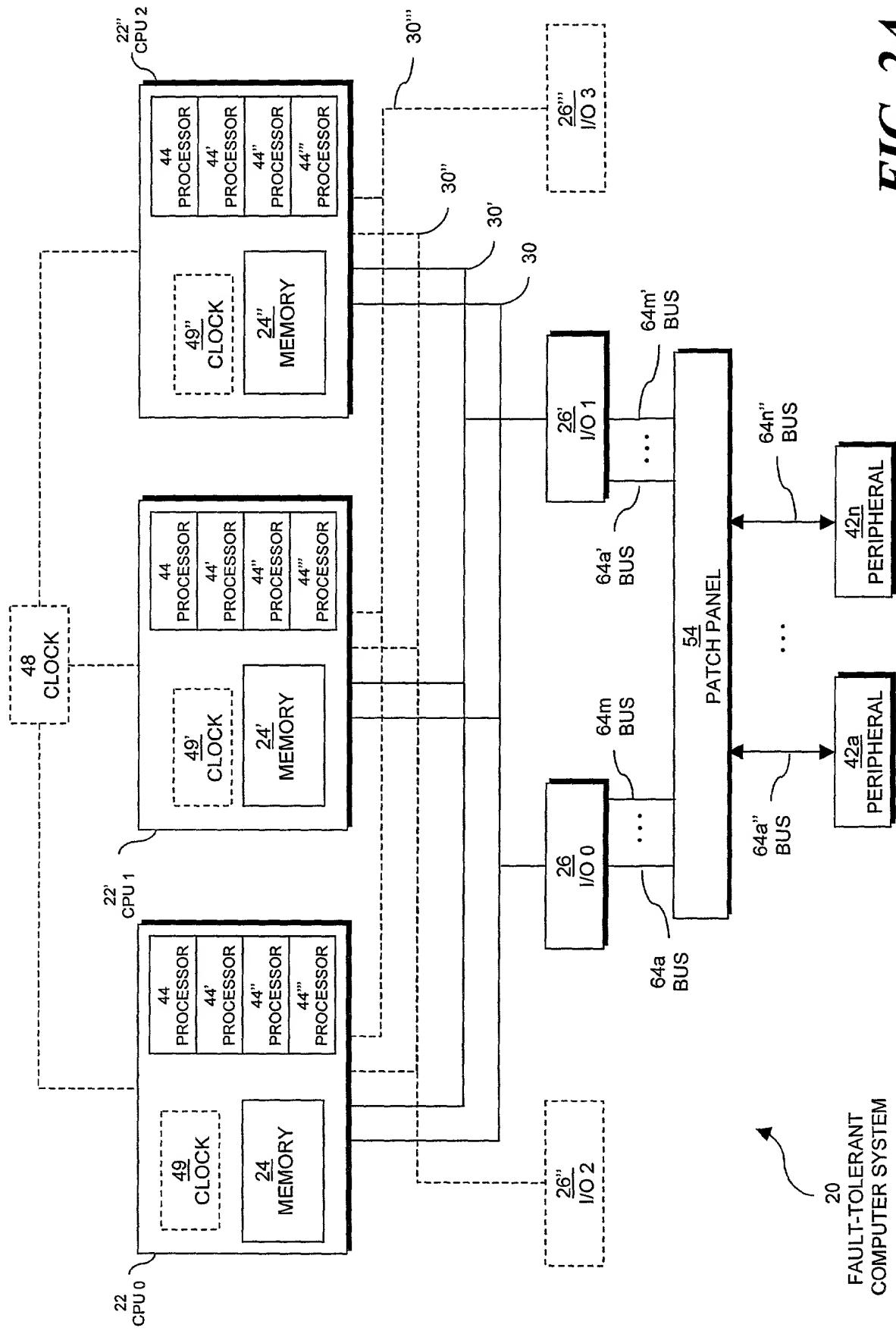


FIG. 2A

20 COMPUTER SYSTEM

56

30 COMMUNICATIONS LINK

58

42 PERIPHERAL DEVICES

22 CPU

60 BRIDGE

60' BRIDGE

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graph TD; CS[20 COMPUTER SYSTEM] --> CL[30 COMMUNICATIONS LINK]; CS --> B1[60 BRIDGE]; B1 --- CPU_BUS[CPU BUS]; CPU_BUS --- CPU[22 CPU]; B1 --- CL; B2[60 BRIDGE] --- IOBUS[I/O BUS]; IOBUS --- PD[42 PERIPHERAL DEVICES]; B2 --- CL; wavy1[56] --- CPU_BUS; wavy2[58] --- IOBUS;
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PRIOR ART

FIG. 2B

20 FAULT-TOLERANT COMPUTER SYSTEM

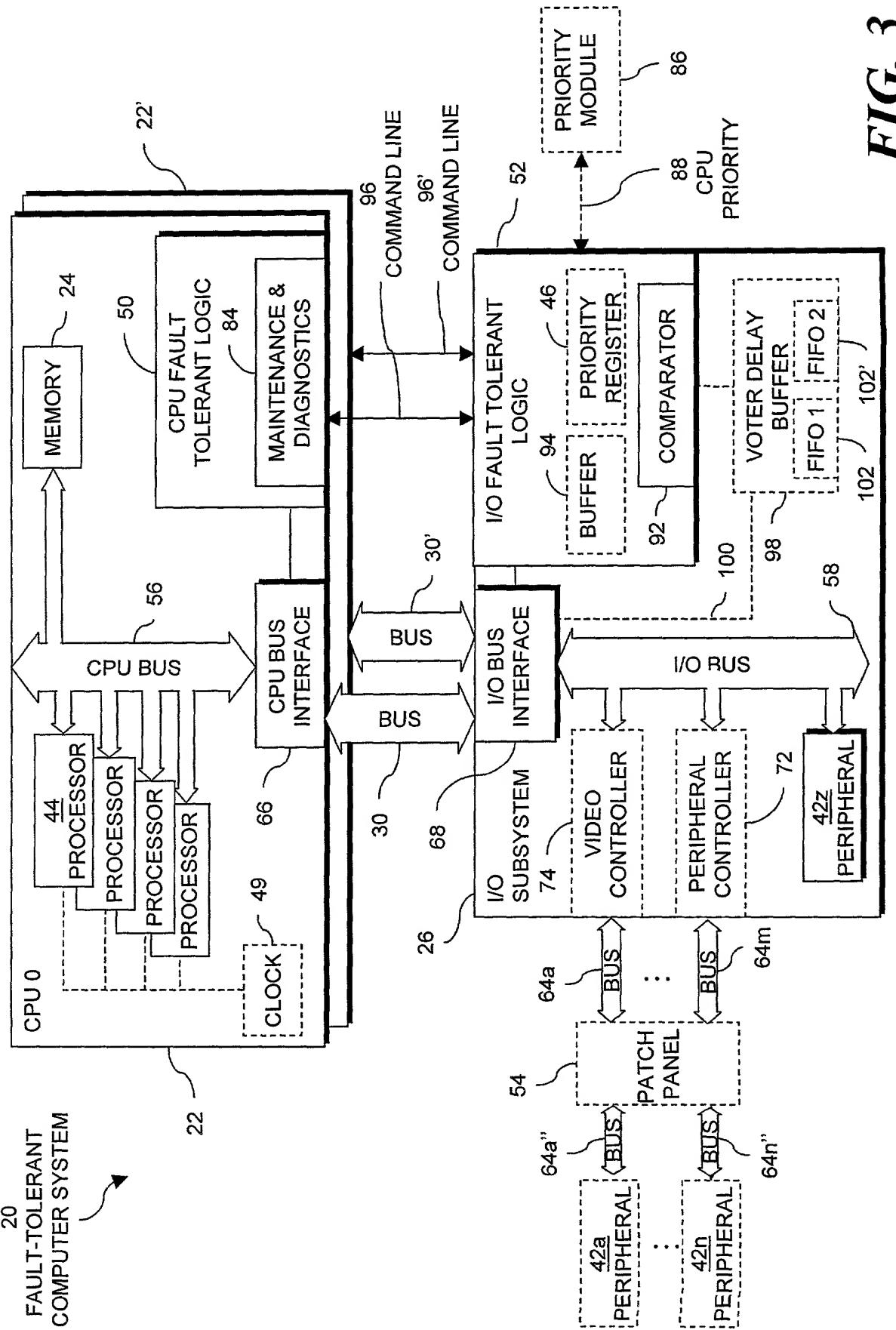


FIG. 3

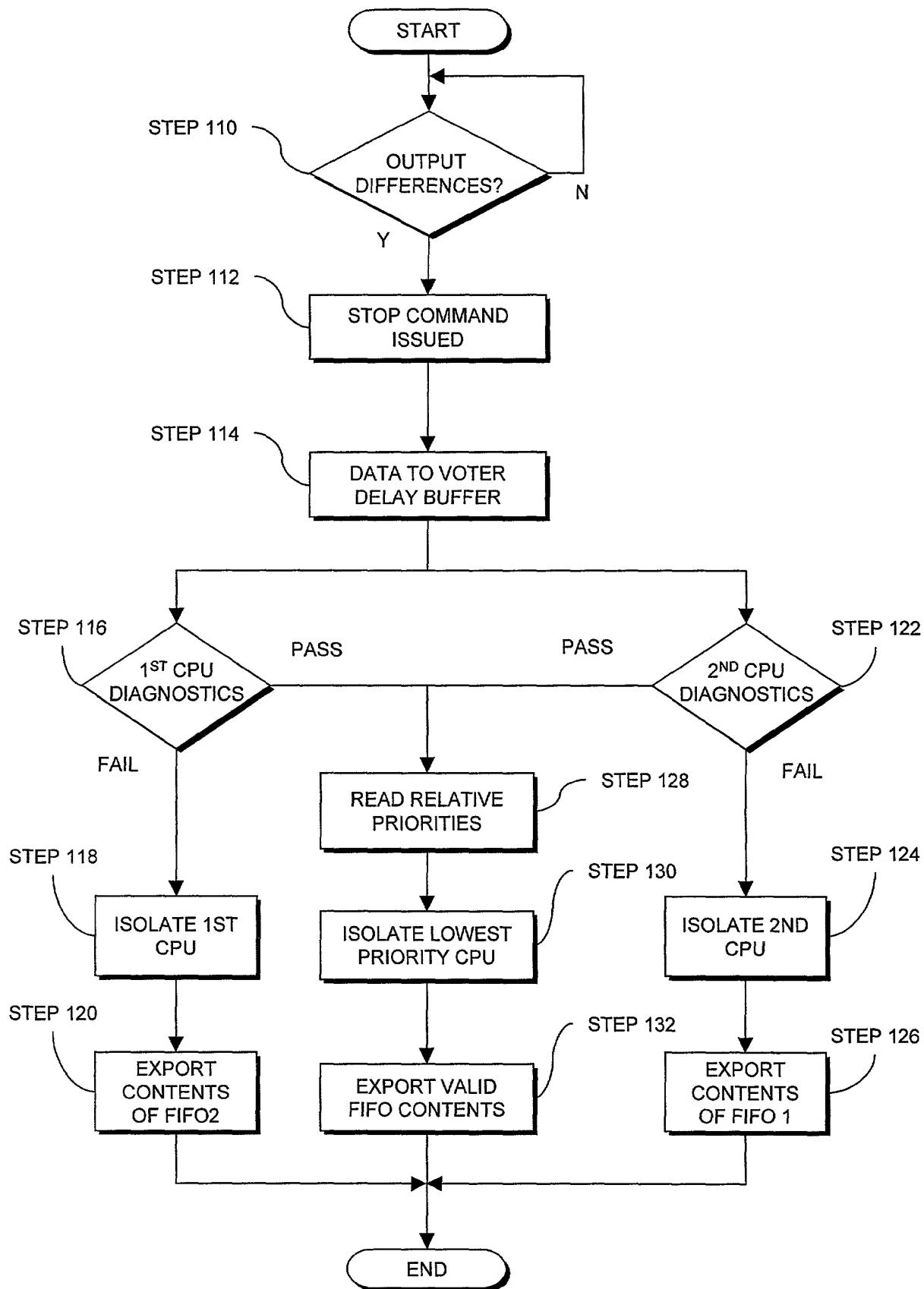


FIG. 4

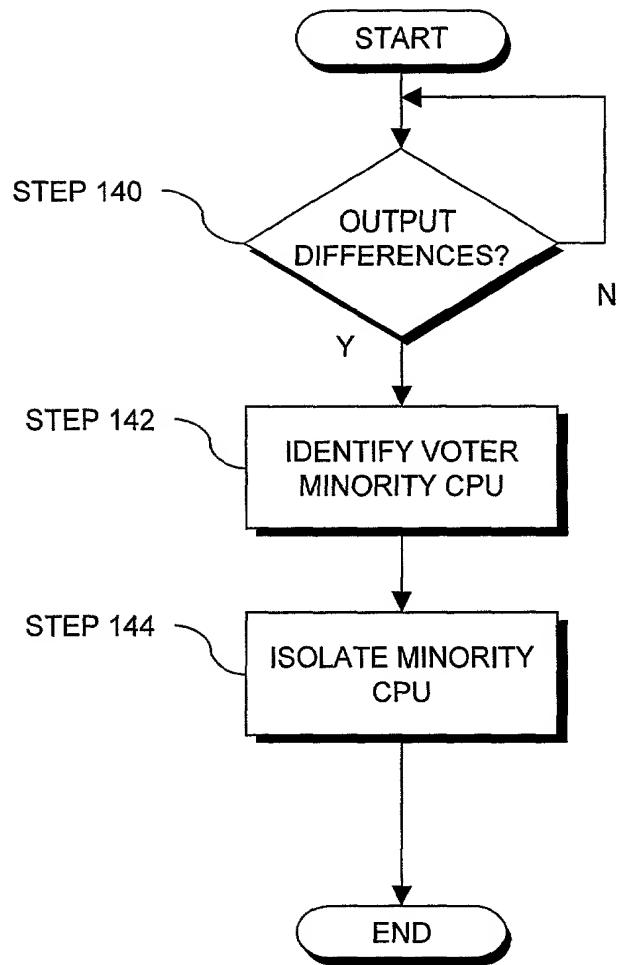


FIG. 5

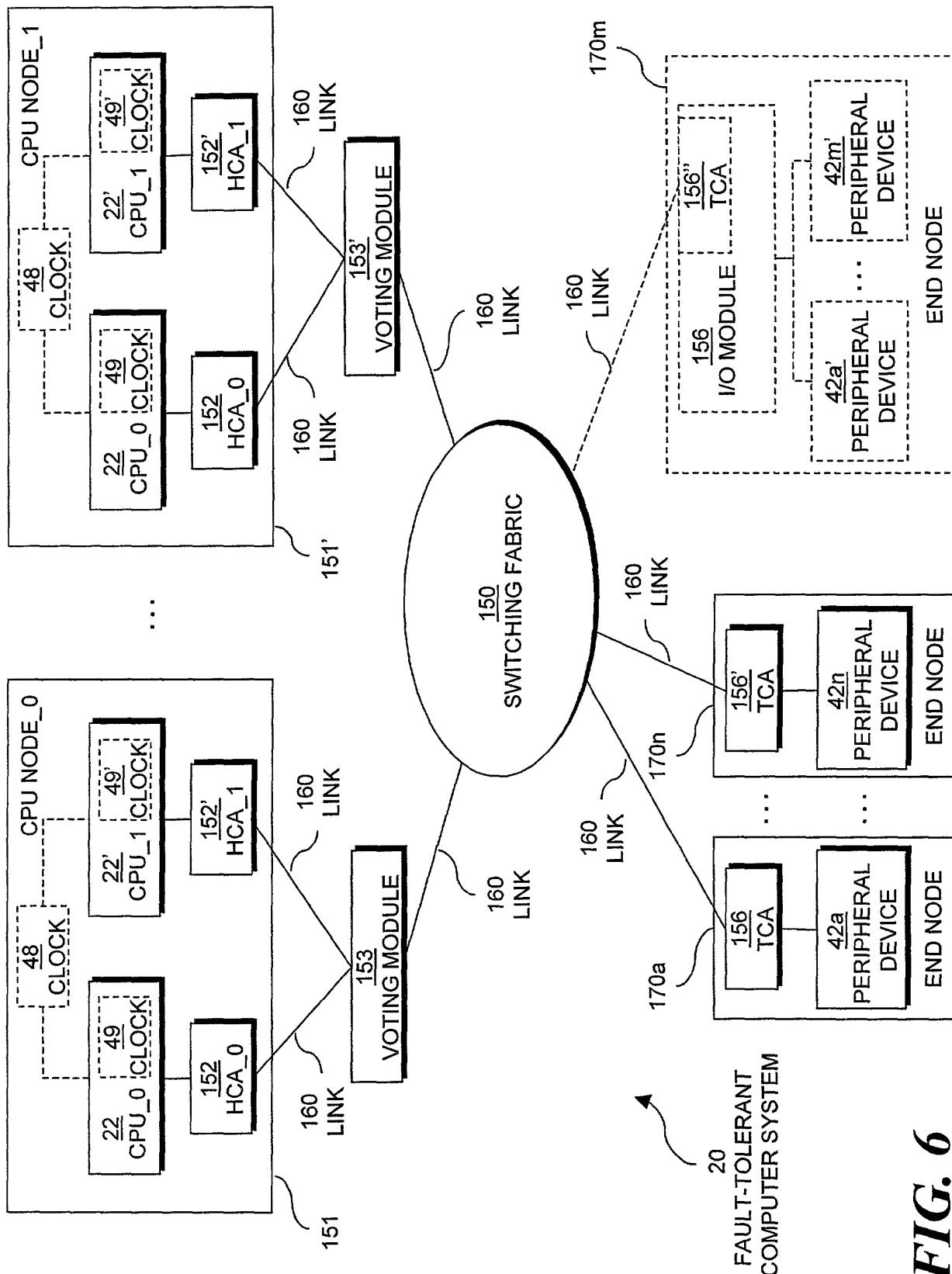


FIG. 6

